

Claims

[c1] What is claimed is:

1.A keyboard comprising:

a key module comprising at least one key cell with an output end being selectively connected to a first voltage or a second voltage;

a detect circuit electrically connected to the output end of the key cell for generating a control signal whenever the voltage on the output end of the key cell becomes the second voltage or the first voltage;

a parallel-to-serial register electrically connected to the output end of the key module; and

a processor electrically connected to the parallel-to-serial register and the detect circuit for controlling the parallel-to-serial register according to the control signal.

[c2] 2.The keyboard of claim 1, wherein the detect circuit comprises at least one capacitor corresponding to each key cell within the key module.

[c3] 3.The keyboard of claim 2, wherein the detect circuit further comprises an amplifying circuit for amplifying the voltage in the capacitor.

- [c4] 4.The keyboard of claim 3, wherein the detect circuit further comprises a set of comparators for comparing whether the voltage output from an output end of the amplifying circuit is in a predetermined range and generating the control signal accordingly.
- [c5] 5.The keyboard of claim 4, wherein the set of comparators comprises a positive comparator for generating the control signal when the voltage output from the output end of the amplifying circuit exceeds a positive reference voltage, and a negative comparator for generating the control signal when the voltage output from the output end of the amplifying circuit is lower than a negative reference voltage.
- [c6] 6.The keyboard of claim 4, wherein the detect circuit further comprises an OR gate with its input ends electrically connected to the output ends of the set of comparators, and its output end for outputting the control signal.